

Figure 1
 (prior art)

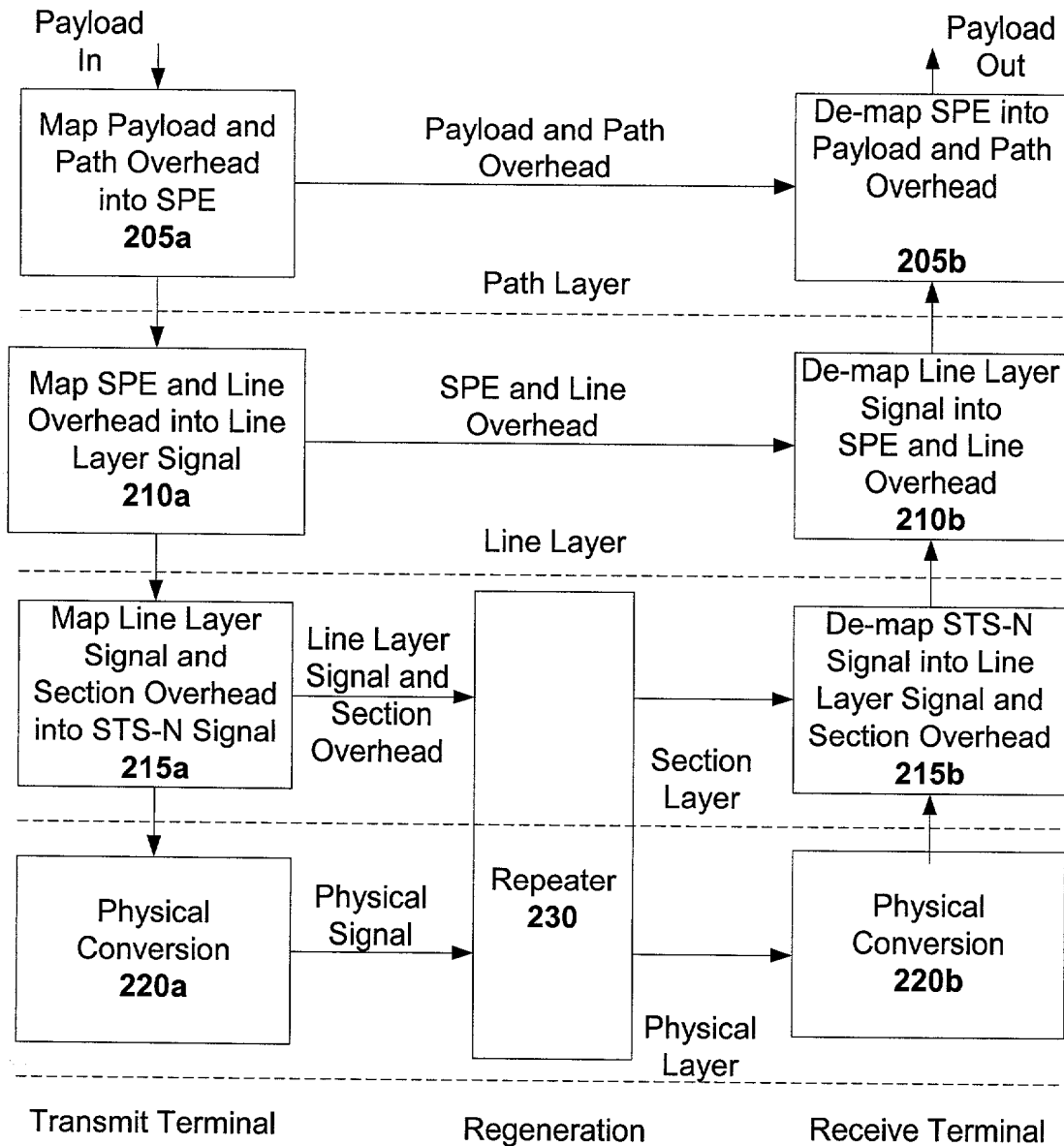


Figure 2
 (prior art)

Transport Overhead

Section Layer Overhead	Transport Overhead		
	A1 - Framing	A2 - Framing	J0 - Trace
	B1 - BIP -8	E1 - Orderwire	F1 - User
Line Layer Overhead	D1 - Data Com	D2 - Data Com	D3 - Data Com
	H1 - Pointer	H2 - Pointer	H3 - Pointer Action
	B2 - BIP -8	K1 - APS	K2 - APS
	D4 - Data Com	D5 - Data Com	D6 - Data Com
	D7 - Data Com	D8 - Data Com	D9 - Data Com
	D10 - Data Com	D11 - Data Com	D12 - Data Com
	S1 - Sync Status	M0 or M1 - REI -L	E2 - Orderwire

BIP - Bit Interleaved
 APS - Automatic Protection

Figure 3
 (prior art)

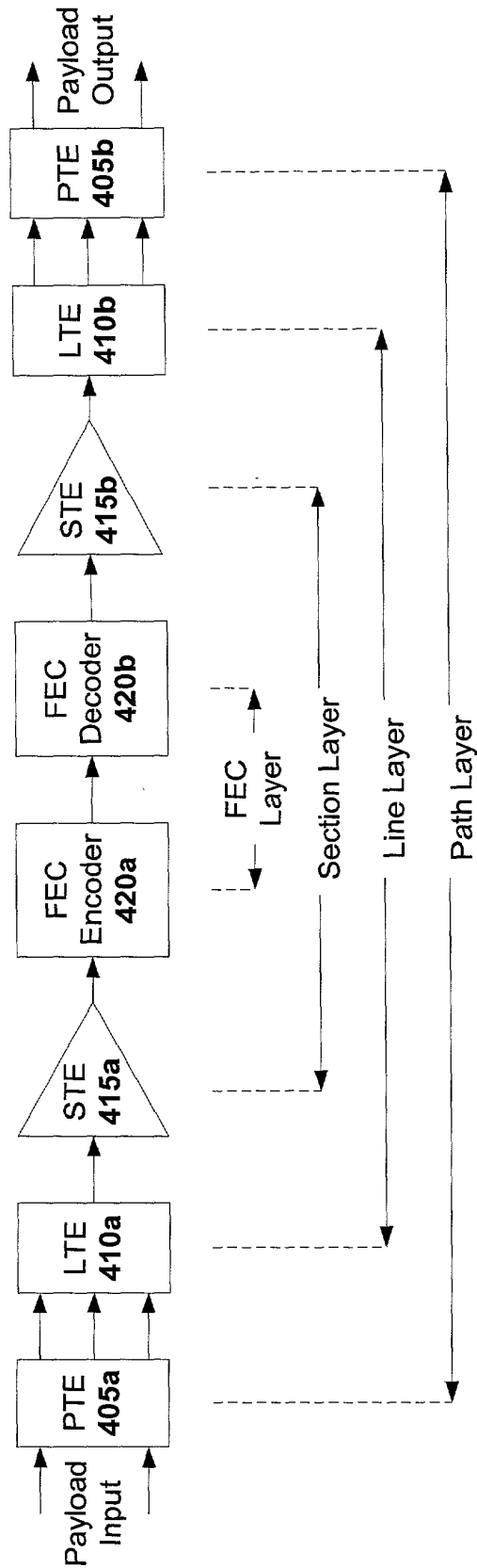


Figure 4

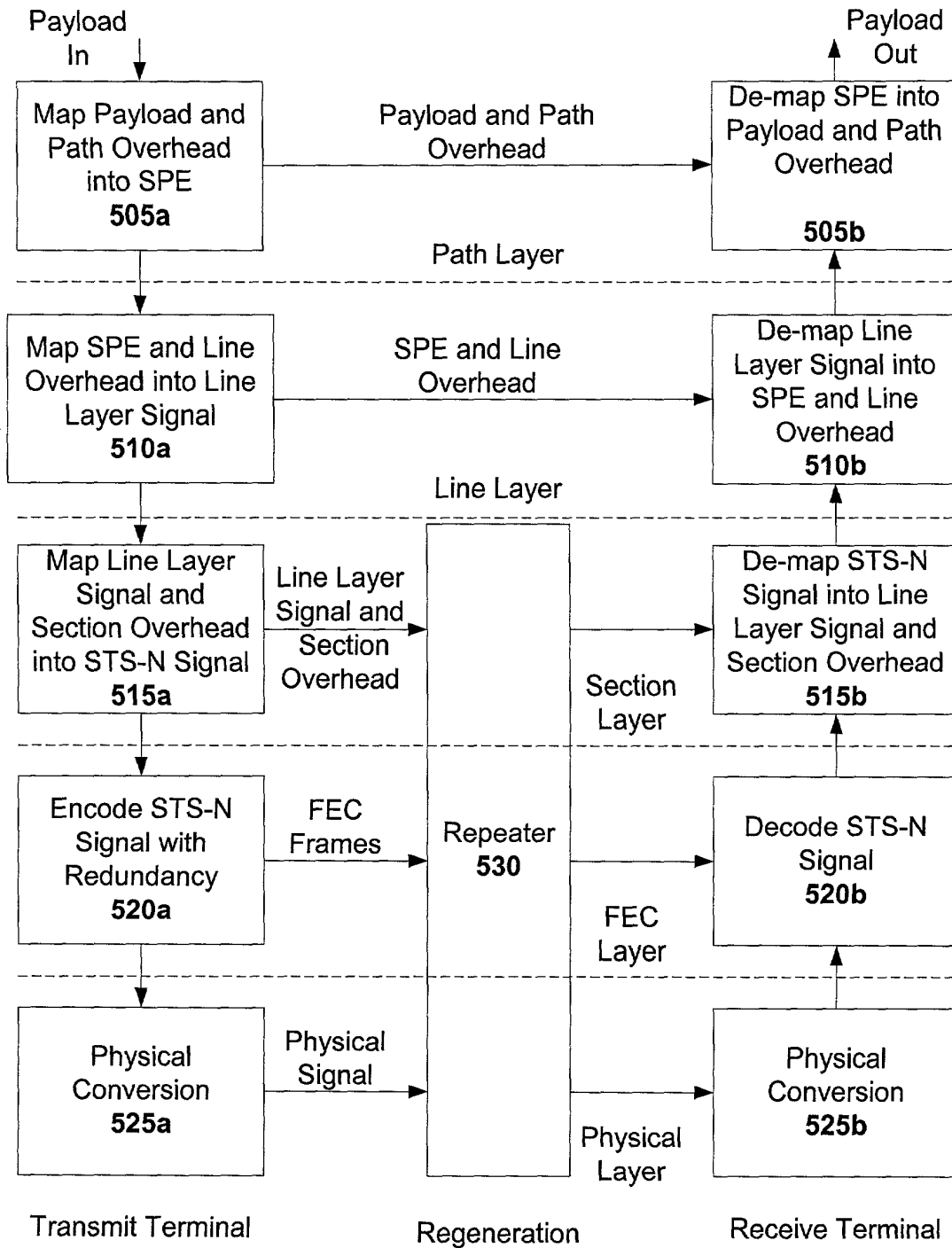


Figure 5a

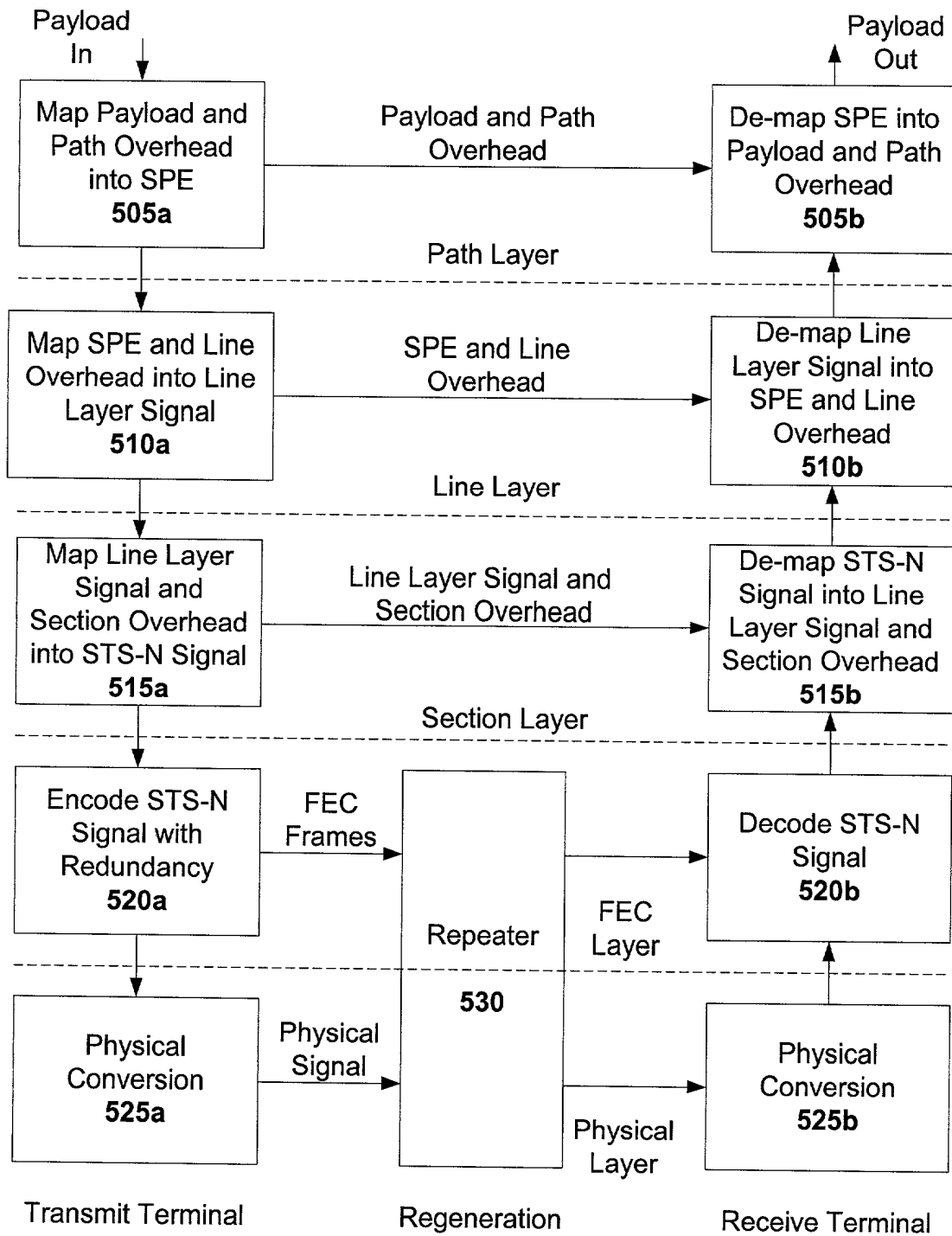


Figure 5b

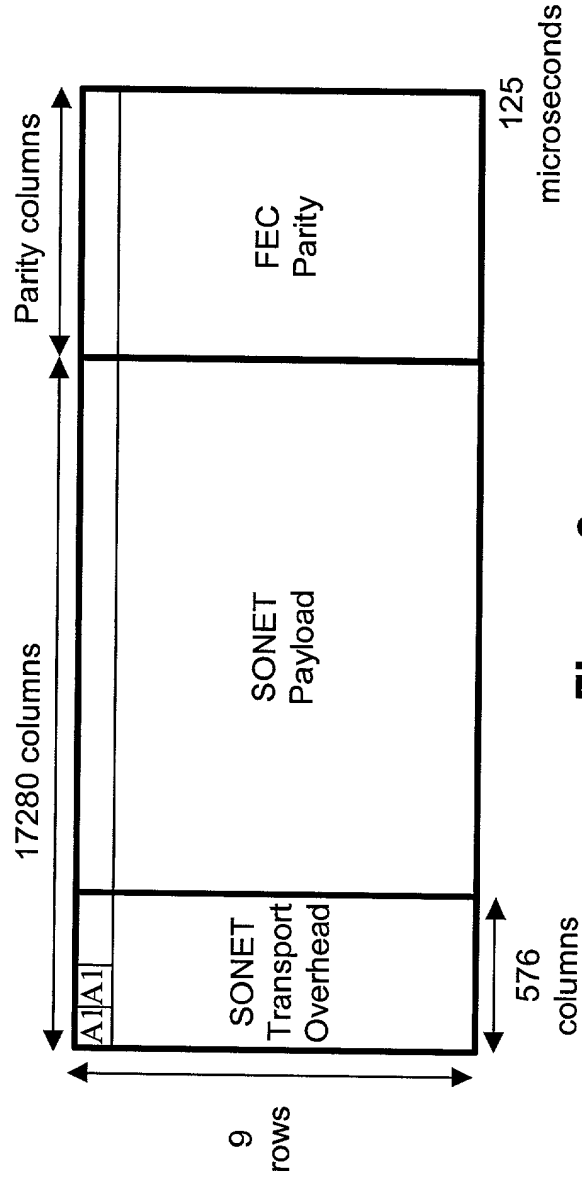


Figure 6

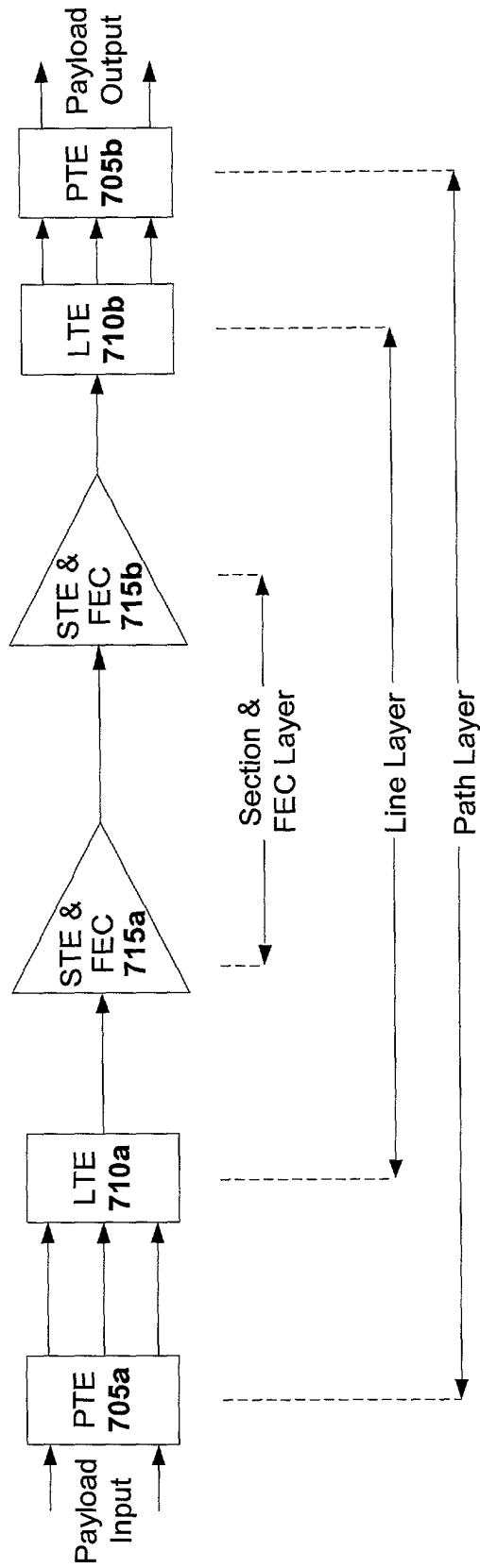


Figure 7

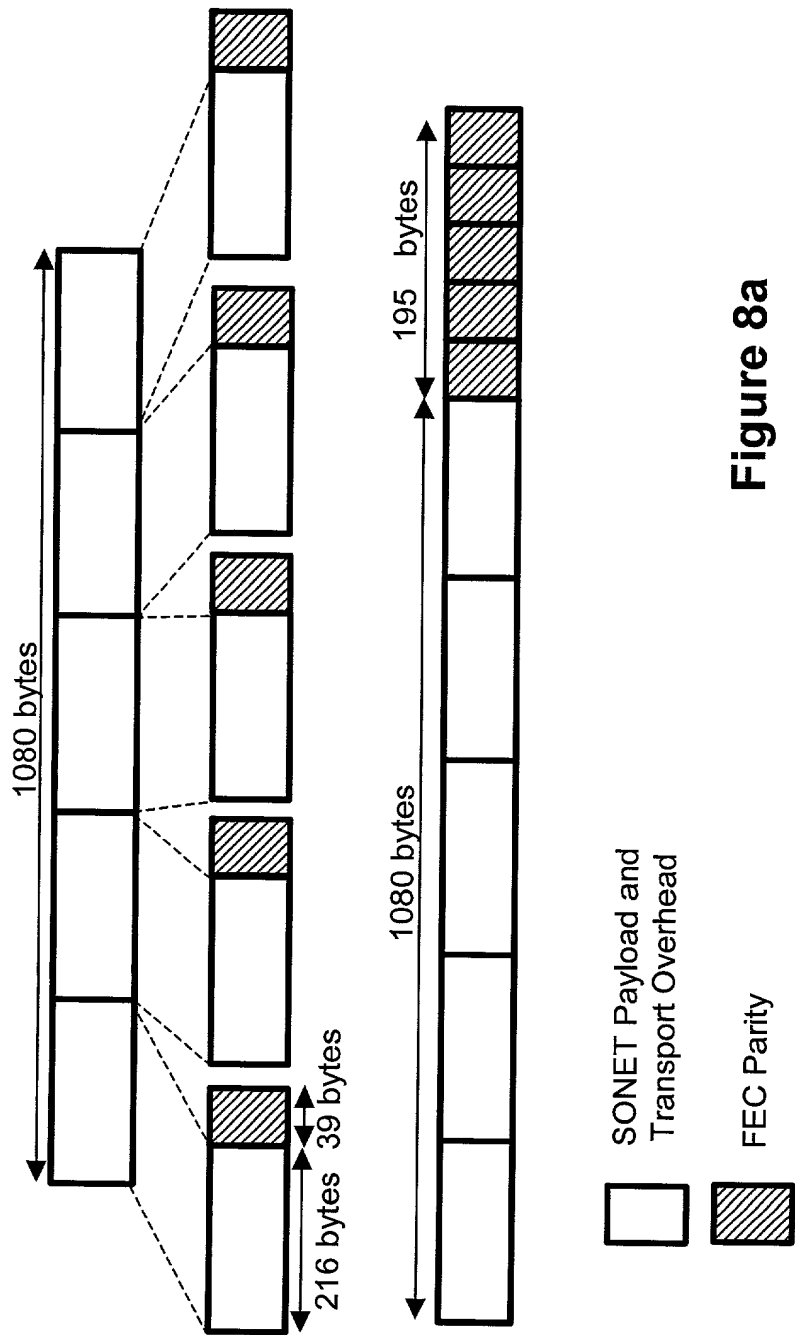


Figure 8a

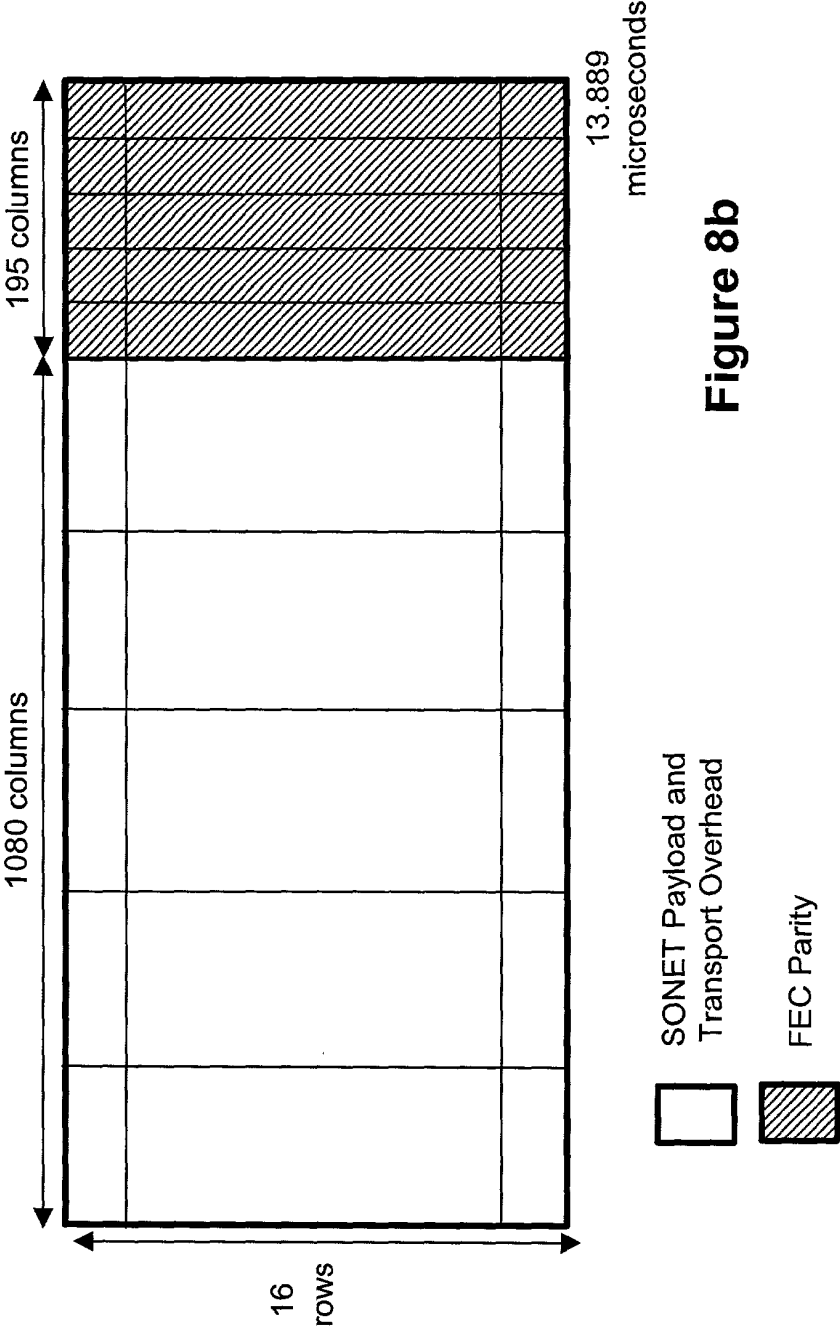


Figure 8b

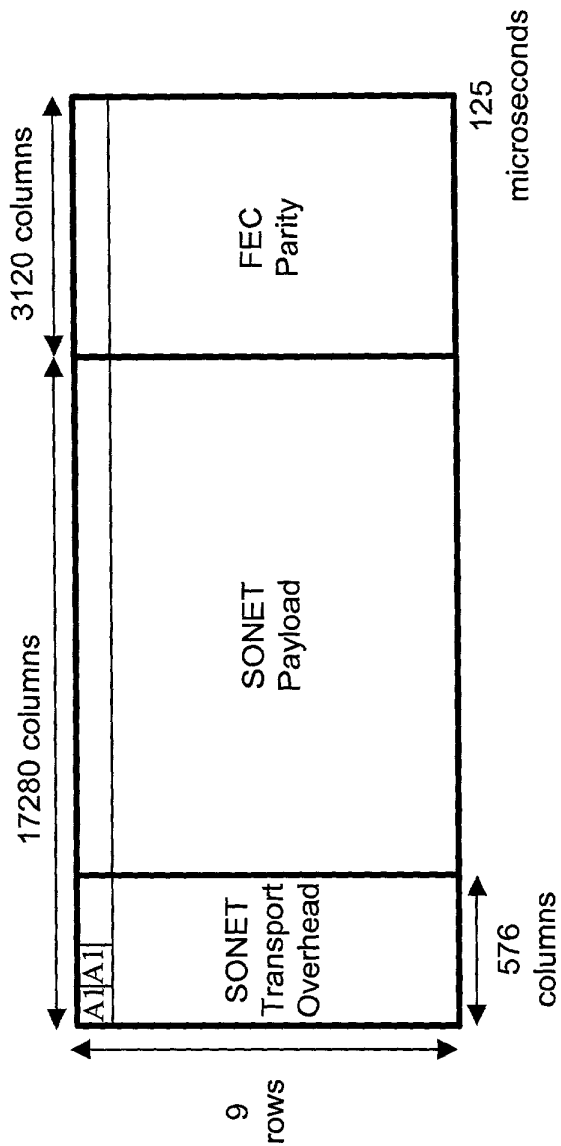


Figure 8c

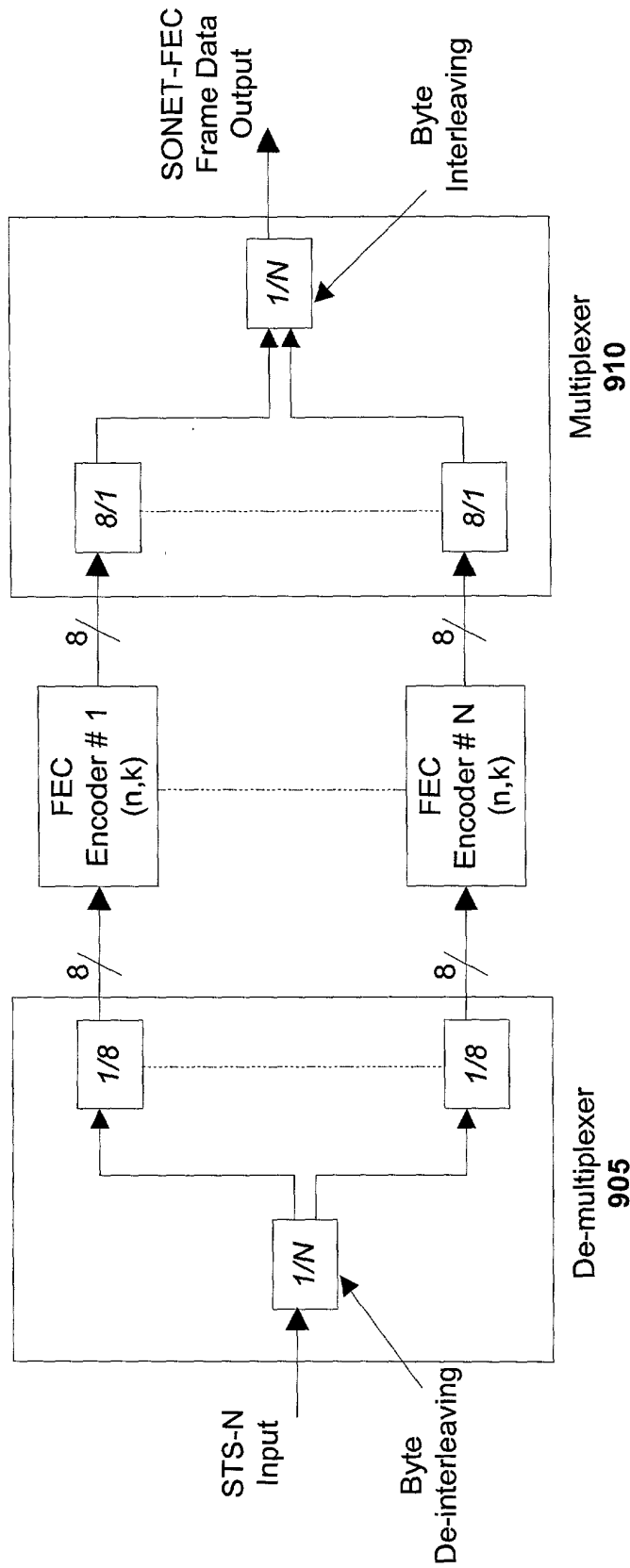


Fig. 9

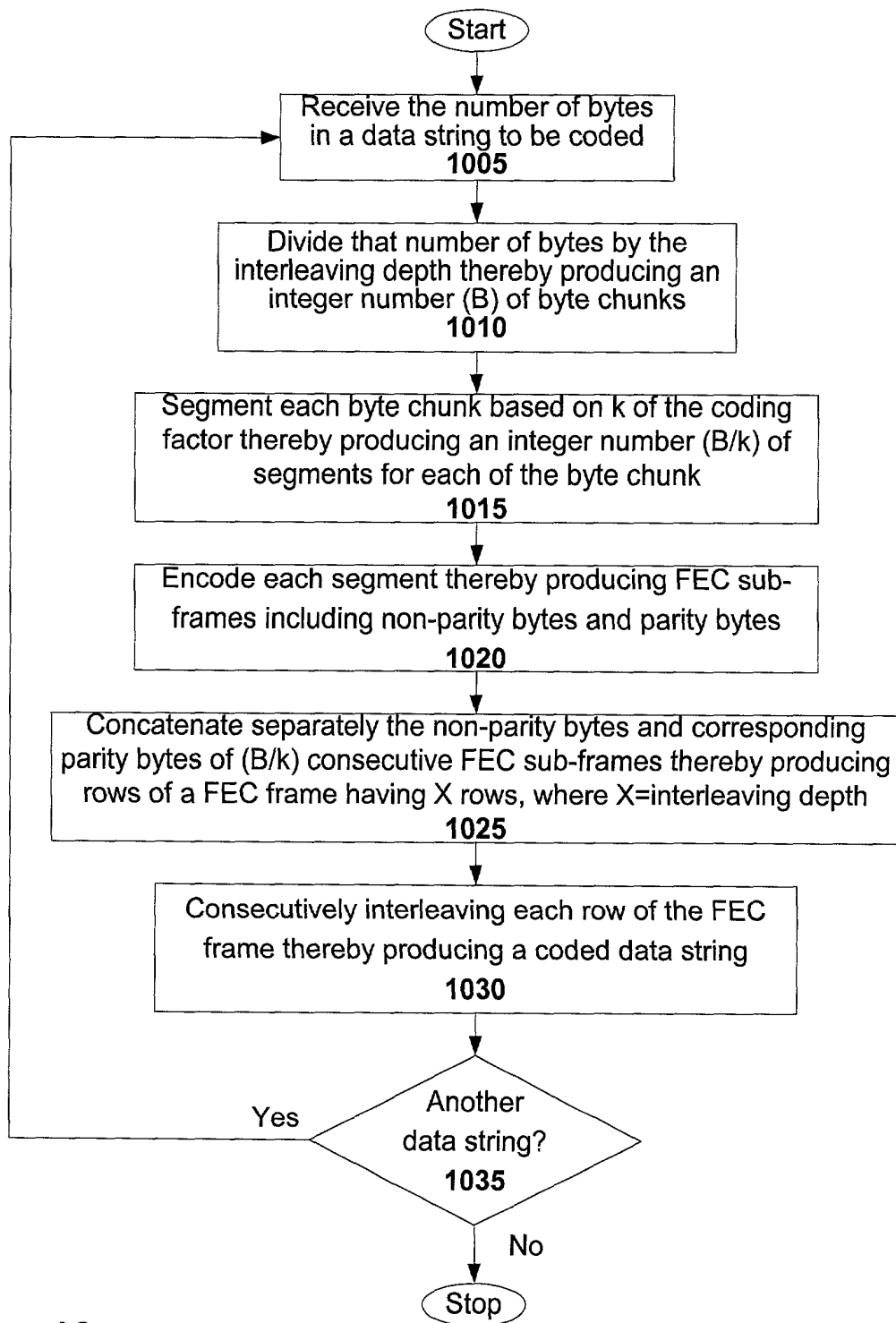


Fig. 10